Readme for 82579 GbE NVM

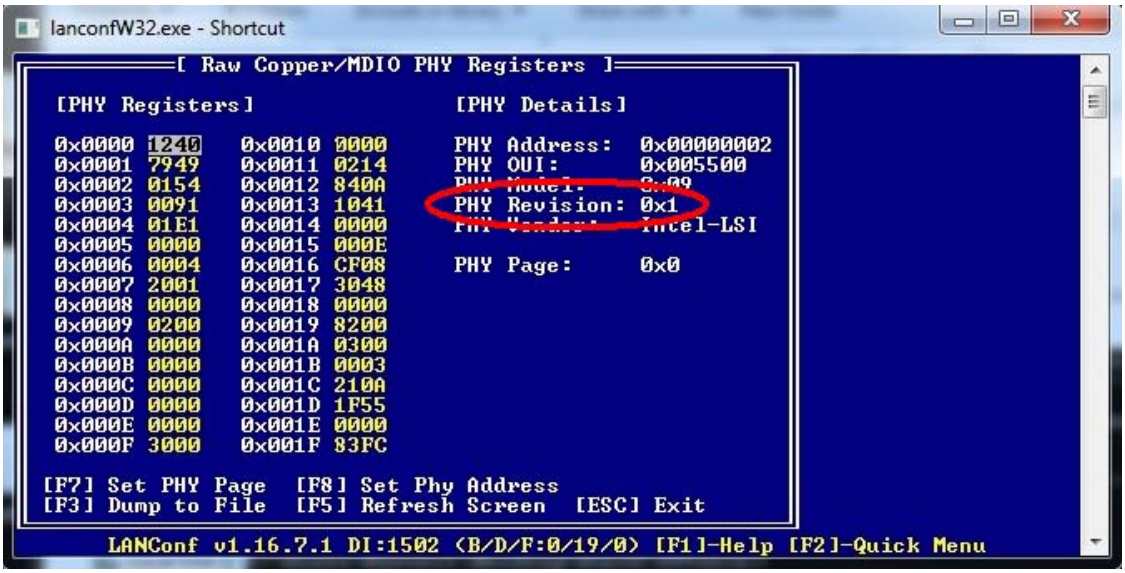
Care should be taken to use the correct GbE NVM (Non-Volatile Memory) firmware revision for the stepping combination of the Cougar Point/Patsburg and the 82579. The table below lists the NVM revision that is optimized for use with the silicon stepping combination.

GbE NVM (.txt and .bin files)	**82579 NVM Ver.**	**Steppings**	**NVM Notes**
• 82579 With LAN switch	**0.D3**	**82579 C0 / CPT B1** **(or)** **82579 B0 / CPT B1** **(or)** **82579 A2 / CPT B0**	For mobile designs with LAN switch
• 82579 Without LAN switch	**0.D4**		For designs (mobile, desktop, server) without LAN switch

Note: See below for detail on identifying PHY & CPT versions.

How to identify the PHY stepping in the system?

Use the latest version of LANCONF (Intel LAN tool found in the SVK (Sample Validation Kit)). Navigate to *Registers MAC and PHY Registers -> PHY Registers Menu -> Raw PHY Registers.* On the *Raw PHY Registers* screen, look at the PHY revision field on the right side (See picture):
0x0 indicates => 82579 A-1 stepping is on the system
0x1 indicates => 82579 A-2 stepping is on the system
0x2 indicates => 82579 B-0 stepping is on the system
0x3 indicates => 82579 C-0 stepping is on the system



How to identify the Ibex Peak stepping?

Use a PCI tool to read the LPC Device ID D31:f0 offset 08h. If the byte value in the field reads:
00h: CPT A0 stepping
01h: CPT A1 stepping
02h: CPT B0 stepping
03h: CPT B1 stepping

For more information, please refer to Cougar Point External Datasheet Specification.